Docket No.: SMQ-067/P5716

App No.: 10/016196 Docket No.: SMQ-06 Inventor: William B. GIST et al.
Title: SINGLE-ENDED IO WITH DYNAMIC SYNCHRONOUS

DESKEWING ARCHITECTURE REPLACEMENT SHEET

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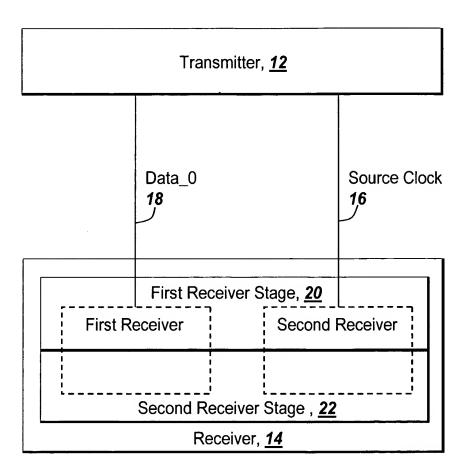


Fig. 1

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Title: SINGLE-ENDED IO WITH DYNAMIC SYNCHRONOUS DESKEWING ARCHITECTURE REPLACEMENT SHEET APR 2 0 2005 2/3 ENT & TRADEN 22 Second Receiver Stage Integration Amplifier 26 Deskewing Circuit **5**6 28 40 Detection Circuit Fault Detector Control Circuit Charge Pump .52 Loop Filter Phase Detector Counter Phase Locked Loop Circuit/Delay-Locked Loop Circuit 50 30 44 Delay Circuit Controlled Delay Line \subseteq Frequency Multiplier Voltage Controlled Oscillator/Voltage Phase Detector Delay Elements 34B Delay Elements FSM 23B **23A** First Receiver Stage Fig. 2

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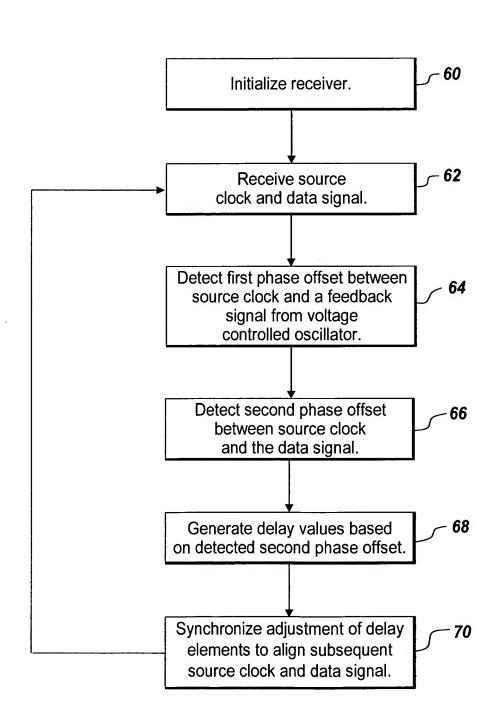


Fig. 3